Tulsiramji Gaikwad-Patil College of Engineering & Technology, Nagpur

SCHEME OF INSTRUCTION

Programme: Electronics & Communication Engineering

Scheme of Instructions: Second Year B. Tech. in Electronics & Communication Engineering

Semester - III

								Carlo Colonia		EXAM SCHEM		EME	
Sr. No.	Course Category	Code	Course Title	L	T	P	Contact Hrs/Wk	Course Credits	CT-	CT-2	TA/CA	ESE	TOTAL
1	BSC	EC2301	Electrical and Electronics Engineering Mathematics	3	*		3	3	15	15	10	60	100
2	ESC	EC2302	Instrument and Measurement	3	100	-	3	3	15	15	10	60	100
3	PCC	EC2303	Object Orientated Programming Structure	3	1	2.0	3	3	15	15	10	60	100
4	PCC	EC2304	Network Analysis and Synthesis	3	1	-	4	4	15	15	10	60	100
5	PCC	EC2305	Digital Electronics & Memories	3	-		3	3	15	15	10	60	100
6	ESC	EC2306	Electronics Devices & Circuit	3	1		4	4	15	15	10	60	100
7	PCC	EC2307	Object Orientated Programming Structure Lab	-	247	2	2	1	-	(A)	25	25	50
8	ESC	EC2308	Electronics Devices & Circuit Lab	-	8	2	2	1	-	-	25	25	50
9	PCC	EC2309	Digital Electronics and Memories Lab	-	-	2	2	1	-		25	25	50
10	ESC	EC2310	Instrument and Measurement Lab	-	-	2	2 .	1	-		25	25	50
11	MCC	AU2310	Environmental Science	2	-	-	2	Audit	-			-	-
			Total	20	02	08	30	24	90	90	160	460	800

L- Lecture

T-Tutorial

P-Practical

CT1- Class Test 1 CT2- Class Test 2 TA/CA- Teacher Assessment/Continuous Assessment
ESE- End Semester Examination (For Laboratory End Semester performance)

	C14	- Class I est	***	LIGHT LING DWIN	distribution that the second	, hallach eil en das der	obstantio de de la constantia del constantia de la constantia de la constantia della constantia della consta	
Course Category	HSMC (Hum., Soc. Sc, Mgmt.)	BSC (Basic Sc.)	ESC (Engg. Sc.)	PCC (Programme Core Courses)	PEC (Programme Elective Courses)	OEC (Open Elective courses from other discipline)	Project / Seminar /Industrial Training	MCC (Mandatory Courses)
Credits		03	09	12	**			Yes
Cumulative Sum	5	18	15					

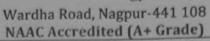
PROGRESSIVE TOTAL CREDITS: 35+24=59

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Tulsiramji Gaikwad Patil College Or
Engineering and Technology, Nagori









Second Year (Semester-III) B.Tech. Electronics & Communication Engineering

	BEC2301: Electrical and Elec	tronics Engineering Ma	thematics	
Teaching S		Examinatio	n Scheme	
Lectures	3Hrs/week	CT-1	15 Marks	
Tutorial	1Hrs/week	CT-2	15 Marks	
Total Cred	it 4	TA 10 Marks		
		ESE	60 Marks	
		Total	100 Marks	
	Duration of ESE: 02 Hrs 30			
Course Ou	teomes:			
Student wil	Il be able to			
1	Analyze numerical techniques to fit	nd the roots of equations diffe	rent types of equa	tions.
2	Apply the concept of Laplace Trans	sform for Solving differential	equation	
3	Apply the knowledge of Fourier ser and solve integral equations.	ries and Transform for unders	tanding periodic si	ignals
4	Solve Partial Differential Equation	using appropriate method		
5	Applythe concept of Z-Transform f		1	
	Course Co			Hours
	NUMERICAL METHODS:Error		Errors in series	
	approximation, Rounding of errors			
	Equation: Bisection method, False			(9)
Unit I	and their convergence, Solution of			(2)
	Gauss elimination method, Gauss J method.			
	Laplace Transform: Definition, F Transform, Inverse Laplace Transf	form and its Properties, Conv	olution theorem	
Unit II	(statement only), Laplace Transform Step Function and Unit Impulse Fu solve Ordinary Differential Equation	nction, Applications of Lapla		(9)
Unit III	Fourier Series & Fourier Trans Expansions, Even and Odd fur Expansions. Fourier Transform: Fourier Integral Theorem, Relation Fourier Transform to Solve Integral	nctions, Change of interval Definition and Properties (e on with Laplace Transform,	al, Half Range excluding FFT),	(9)

Unit IV	Partial Differential Equations: Partial Differential Equations of First Order First degree i.e.Lagrange's form, Linear Homogeneous Equations of Higher order with constant coefficients, Method of separation of variables, Applications of Partial Differential Equations Introduction to Mathematical Modelling	(9)
Unit V	UNIT - III: Z-TRANSFORM Definition, Convergence of Z-transform and Properties, Inverse Z-transform by Partial Fraction Method, Residue Method (Inversion Integral Method) and Power Partial Fraction Method, Convolution of two sequences. Solutions of Difference Equations with Constant Coefficients by Z- transform.	(9)
Text Books		
1	Higher Engineering Mathematics by B.S. Grewal, 40th Edition, Khanna Publication	
2	Advanced Engineering Mathematics by Erwin Kreysizig, 8th Edition, Wiley India	133
- 3	Applied Mathematics for Engineers & Physicist by L.R. Pipes and Harville.	
Reference B		-
1	A Text Book of applied Mathematics, Volume II, by P.N. Wartikar& J.N. Wartikar, Poona VidyarthiGrihaPrakashan	
2	Introductory methods of Numerical Analysis, by S.S. Sastry, PHI	
3	Mathematics for Engineers by Chandrika Prasad John wiley& son	

Perment of Electronics & Comm. Tuistraniji Galkwad - Pari! Cullege of Engineering & Technology, Nagpur

Principal

Dean Academics Vice Principal
Tulsiramji Gaikwad-Patil Tulsiramji Gaikwad-Patil
College Of EngineeringCollege Of Engineering Usiramji Gaikwad Patil College
and Technology, Nagpur Techn



Technology

Wardha Road, Nagpur-441 108 NAAC Accredited (A+ Grade) G

Semester	Course Code	Name of Course	L	T	P	Credits
III	EC2302	Instrument and Measurement	3	1	_	4
	202002	Course Contents		1		
Unit I	measurement :	on of electronic measurement system, Types of Maystem, Theory of errors, Types of errors, Statistical are securacy and Precision, Standards of measurement.				
Unit II	voltmeters; Pe	Torque and deflection of Galvanometer, PMMC reak, average and true rms; Digital Multimeters: Blum-meters and their design' AC indicating instruments	ock dia	gram	and	specifications
Unit III		Wheatstone Bridge, Kelvin Bridge AC Bridges and Bridge, Anderson bridge, Schering Bridge, Desauty's				
Unit IV	transducer, Re	namic characteristics, Classification of transducers, Casistive transducer, RVDT, Strain Gauge, RTD, Sensi	Capaciti stors, O	ve tra	ansduo l Tran	cer, Inductive
	Level, Displac	eer, Piezoelectric transducers, Transducers for measurement, Flow.	ement o	f Pres	ssure,	Temperature
Uṇit V	Oscilloscope: Sine-wave, sta Harmonic Dis process & Fu		ns of C	CRO. Sign	Signa al An	Temperature Il Generators alyzer: Wave m: Necessity
	Oscilloscope: Sine-wave, sta Harmonic Dis process & Fu channel and m	Dual trace, Digital storage oscilloscopes, Application and AF, RF generator, Pulse generator, Function generator, Spectrum, Logic, digital Fourier analyzer. Data notions of Signal conditioning, AC/DC Conditioning	ns of C	CRO. Sign	Signa al An	Temperature Il Generators alyzer: Wave m: Necessity
Text Book	Oscilloscope: Sine-wave, sta Harmonic Dis process & Fu channel and m S A.D. Helfrick a PHI Publication	Dual trace, Digital storage oscilloscopes, Application and AF, RF generator, Pulse generator, Function generator, Spectrum, Logic, digital Fourier analyzer. Data netions of Signal conditioning, AC/DC Conditioning ulti-channel DAS. and W.D. Cooper: "Modern Electronic Instrumentation is.	ns of C nerator a Acquir g syste	CRO. Sign sition ms, A	Signa al An Syste ADC,	Temperature Il Generators alyzer: Wave m: Necessity DAC, single
Text Book	Oscilloscope: Sine-wave, sta Harmonic Dis process & Fu channel and m S A.D. Helfrick a PHI Publication	Dual trace, Digital storage oscilloscopes, Application and AF, RF generator, Pulse generator, Function generator, Spectrum, Logic, digital Fourier analyzer. Data netions of Signal conditioning, AC/DC Conditioning ulti-channel DAS.	ns of C nerator a Acquir g syste	CRO. Sign sition ms, A	Signa al An Syste ADC,	Temperature Il Generators alyzer: Wave m: Necessity DAC, single
Text Book T.1 T.2	Oscilloscope: Sine-wave, sta Harmonic Dis process & Fu channel and m as A.D. Helfrick a PHI Publication A.K. Sawhney Publications.	Dual trace, Digital storage oscilloscopes, Application and AF, RF generator, Pulse generator, Function generator, Spectrum, Logic, digital Fourier analyzer. Data netions of Signal conditioning, AC/DC Conditioning ulti-channel DAS. and W.D. Cooper: "Modern Electronic Instrumentation is.	ns of C nerator a Acquir g syste	CRO. Sign sition ms, A	Signa al An Syste ADC,	Temperature Il Generators alyzer: Wave m: Necessity DAC, single
T.1 T.2 T.3 Reference	Oscilloscope: Sine-wave, sta Harmonic Dis process & Fu channel and m as A.D. Helfrick a PHI Publication A.K. Sawhney Publications. S.S. Kalsi: "Ele Books	Dual trace, Digital storage oscilloscopes, Application and AF, RF generator, Pulse generator, Function generator, Spectrum, Logic, digital Fourier analyzer. Data netions of Signal conditioning, AC/DC Conditioning ulti-channel DAS. Ind W.D. Cooper: "Modern Electronic Instrumentation is." "Electrical and Electronic Measurement and Instrumentation is decreased and Electronic Measurement and Electronic Measurement and Instrumentation is decreased and Electronic Measurement and Electroni	ns of Conerator Acquire g systemand Me	f Prescale CRO. Sign sition ms, A asurer	Signa al An Syste ADC,	Temperature Il Generators alyzer: Wave m: Necessity DAC, single Techniques", Rai & Sons
T.1 T.2 T.3 Reference	Oscilloscope: Sine-wave, sta Harmonic Dis process & Fu channel and m as A.D. Helfrick a PHI Publication A.K. Sawhney Publications. S.S. Kalsi: "Ele Books Joseph J. Carr: Publications.	Dual trace, Digital storage oscilloscopes, Application and AF, RF generator, Pulse generator, Function generator, Spectrum, Logic, digital Fourier analyzer. Data netions of Signal conditioning, AC/DC Conditioning ulti-channel DAS. Ind W.D. Cooper: "Modern Electronic Instrumentation is." "Electrical and Electronic Measurement and Instrumentation Measurements", Mc Graw Hill Publications. "Elements of Electronic Instrumentation and Measurements"	ns of Conerator Acquire g systemand Mentation"	f Prescale CRO. Sign sition ms, A asurer	Signa al An Syste ADC,	Temperature Il Generators alyzer: Wave m: Necessity DAC, single Techniques", Rai & Sons
T.1 T.2 T.3 Reference	Oscilloscope: Sine-wave, sta Harmonic Dis process & Fu channel and m S A.D. Helfrick a PHI Publication A.K. Sawhney Publications. S.S. Kalsi: "Ele Books Joseph J. Carr: Publications. R.K. Rajput: "	Dual trace, Digital storage oscilloscopes, Application and AF, RF generator, Pulse generator, Function generator, Spectrum, Logic, digital Fourier analyzer. Data netions of Signal conditioning, AC/DC Conditioning ulti-channel DAS. Ind W.D. Cooper: "Modern Electronic Instrumentation is." "Electrical and Electronic Measurement and Instrumentation is decreased and Electronic Measurement and Electronic Measurement and Instrumentation is decreased and Electronic Measurement and Electroni	ns of Conerator Acquire g systemand Mentation"	f Prescale CRO. Sign sition ms, A asurer	Signa al An Syste ADC,	Temperature Il Generators alyzer: Wave m: Necessity DAC, single Techniques", Rai & Sons

1	https://nptel.ac.in/courses/108/108/108108147/
2	https://nptel.ac.in/courses/108/105/108105153/
	Course Outcomes
EC2302.1	Examine measuring methods and instruments of electrical quantities.
EC2302.2	Determine design aspects and performance criterion of measuring instruments.
EC2302.3	Design the different types of bridges.
EC2302.4	Test the working principle of various transducers.
EC2302.5	Analyze CRO, signal generator, wave analyzer and data acquisition system.

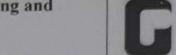
BOS Chairman

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Tulsiramji Gaikwad - Pari College Of Engineering College Of Engineering & Engineering and Technology, Nagpur Technology, Nagpur Technology, Nagpur Technology, Nagpur





Semester	Course Code	tronics & Communication Er	L	T	P	Credits
Ш	EC2303	Object Oriented programming Structure	3		-	3
		Course Contents				
				-		
Unit I	Basic concepts of Programming.	f object oriented programming-Benefits	of OOPs-Ap	plication	OOP S	tructure of Jav
Unit II		Byte Code and Java Virtual Machine, JC if, if-else ladders, Switch, while, do-whi				
Unit III	Recursion, Passir	Object reference, Constructor, Consting and Returning object form Method, Access control, modifiers, Nested	d, new oper	ator, thi	s and s	tatic keyword
Unit IV	Inheritance - me	ce, Inheriting Data members and Met thod overriding, Handle multilevel cons olymorphism: dynamic binding, metho	tructors - su	per keyw	ord, Sto	p Inheritance
Unit V	Exception Handli hierarchy, checke finally, re-throwin Streams: Concept	ing: Benefits of exception handling, to ed exceptions and unchecked exceptions are exceptions, GUI components in Java is of I/O streams, Reading console Input	he classificat ns, usage of Introduction	try, cate try, cate	ch, thro	ns , exceptions , throws an onnectivity. I/O
Text Book	s		and writing	00113010	output,	The Harraning
T.1	Java: The Compl	ete Reference" by Herbert Schildt				1 1 1 1 1 1 1
		th Java" by Balagurusamy			77	70 70 75
		Impatient" by Horstmann				
T.4	Head First Objec	t-Oriented Analysis and Design				
Reference	Books	1 4/37/ 1				
R.1	Thinking in Java b	y Bruce Eckel				78.77
		mers by Paul Deitel, Harvey Deite			-	
R.3	Beginning Java Pro Seppe vanden Bro	ogramming: The Object-Oriented Appro	ach by Bart I	Baesens,	Aimee l	Backiel, and
Useful Lin				28.5		
		ourses/106/102/106102064/		12		
THE RESERVE THE PERSON NAMED IN		ourses/106/106/106106145/				
, 3 1	nttps://nptel.ac.in/c	ourses/106/105/106105085/				
		Course Outco	mes			
	Analyze the co	ncept of OPPS and its importance				
EC2303.1						

EC2303.2	Apply the knowledge of programming and controlling statements in Java.
EC2303.3	Analyze the Concept of programming.
EC2303.4	Apply the knowledge of Inheritance in program development.
EC2303.5	Illustrate the knowledge to handle various exceptions using concepts of exception handling

CHEATTMENT of Electronics & Comm. Tulstramji Gaikwad - Patri College of Engineering & Technology, Nagpin Dean Academics

Dean Academics Tulsiramji Gaikwad-Patil Tulsiramji Gaikwad-Patil

And Technology, Nagpur Technology, Nagpur.

Vice Principal

Principal Principal Principal

College Of Engineering College Of Engineering & Isiramji Gaikwad Patil College of Technology No.



Technology

		onics & Communication Engineer	ing			
Semester	Course Code	Name of Course	L	T	P	Credits
III	EC2304	Network Analysis & Synthesis	3	1	-	4
		Course Contents	-			
Unit 1	equivalent sources,	reuits, circuit elements and their voltage - source transformation, Mesh & Nodal Ar ention in coupled circuits	curren alysis,	nt rela Dual	tionship ity, Mu	o, concept o
Unit II	Superposition Theorem	orem, Thevenin's Theorem, Norton's Theorem,	rem, N	/axim	um Po	wer Transfe
Unit III	Ac circuit analysis circuits, Significan Selectivity, Magnif	with dependent current and voltage source of Quality factor, Bandwidth, Select ication factor.	es, Seivity,	ries ar Effect	of Rg	llel resonan on BW &
Unit IV	synthesis, analysis	s and properties, partial fractions, sir of RC, RL, and RLC networks with and Evaluation of initial conditions.	witho	ty fur ut init	nctions, ial con	waveform ditions with
Unit V	Two port network Introduction to band	and interconnections, Benavior of series d pass, low pass, high pass and band reject	and p	aralle	reson	ant circuits,
Text Book	S		-			
T.1	M.E. Van Valkenbu	urg : Network Analysis, PHI				1
T2		: Network and systems, New Age Publicat	ion.			
T.3		eory: Kelkar and Pandit, Pratibha Publicat		790	DA E	
Reference		and I did I	10115.	-		444.00
R.1	Circuit Theory · Ch	akraborti , Dhanpat Rai				
R.2		Analysis: Hayt W.H. & J.E. Kemmerly,	TMII	-		
R.3		rith Applications: William D Stanley, Pear				
Useful Lin		Thi Applications : William D Stanley, Pear	son Ed	ucano	n	
1		courses/108/105/108105159/	-	-		
2		courses/108/102/108102042/				
3	https://onlinecours	ses.nptel.ac.in/noc20_ee46/preview				
		Course Outcomes				
EC2304.1	Apply the knowle	dge of electrical circuits with nodal and me	sh ana	lysis.		
EC2304.2	Apply various net	work theorems for the analysis of electrical	circui	ts.		
EC2304.3	Examine Ac circu	it and Significance of Quality factor, Band	width,	Select	ivity.	

EC2304.4	Evaluate steady state and transient analysis with Laplace Transform.	
EC2304.5	Analyze different Filters and Attenuators	

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Tulsiramji Gaikwad-Patil Tulstramiji Galkwad - Pani Cullege College Of Engineering Engineering & Technology Nagput and Technology, Nagpur

Dean Academics Dean Academics

Vice Principal Vice Principal

Principal

Principal

Tulsiramji Gaikwad-Patil College Of Engineering Sulsiramji Gaikwad Patil College Technology, Nagpur. Engineering and Technology, Nag





The same of the sa		onics & Communication Engineerin Name of Course	L	T	P	Credits
Semester	Course Code	Name of Course				
111	EC2305	Digital Electronics & Memories	3	-	-	3
		Course Contents				
Unit I	Conversion, Digital De morgan's Theore	& Digital Signals, Number system, Nu	plemen	t,	,0010	an riigeore
Unit II	POS forms), minir variables), don't ca segment decoder, (Comparator, Multip Demultiplevers, Enc.	tions for logic functions, k map representation of logical functions for min-terminations, Design Examples: Arithmetical converters. Adders and their use a plexers and their use in combinational logicoders & Decoders	etic Ci is subs	rcuits, stractor gns, n	BCI r, AI nultip	D - to - LU, Digital lexer trees
Unit III	clear terminals Eve	Clocked SR, JK, MS J-K flip flop, D and T citation Table for flip flops. Conversion of	of Hip	ops. U flops.	Regi	sters, Shi
	registers, Counters	s (ring counters, twisted ring counters)	ers),	seque	ice	Generator.
Unit IV	Types of Memory of Programmable logic complex Programma	commonly used memory chips. Programma de devices (PLD), Programmable logic array	able Lo	egic D ramma	evice able a	s: ROM a array logic FPGA)
	Types of Memory of Programmable logic complex Programmable Basic design steps-	commonly used memory chips. Programma devices (PLD), Programmable logic array	hronoughble Logy, Prograble Constants	ogic Dramma Gate Andressignr	evice able a rray (s: ROM a array logic FPGA)
Unit IV Unit V	Types of Memory of Programmable logic complex Programmable Basic design steps-Moore machines rep Sequence detector.	down counters, twisted ring counters. Asynctronous counters. Asynctronous counters. Asynctronous counters. Asynctronous counters. Asynctronous counters. Programma de devices (PLD), Programmable logic array able logic devices (CPLDS), Field Programma State diagram, State table, State reduction, presentation, Implementation, finite state management	chronou able Lo y, Prog nable C State a	ogic Drammadate Amassignr	evice able a rray (s: ROM a array logic FPGA)
Unit IV Unit V	Types of Memory of Programmable logic complex Programmable Basic design steps-Moore machines rep Sequence detector.	lown counters, twisted ring counters lown counters, synchronous counters. Asynchronous counters. Asynchronous counters. Asynchronous counters. Programma de devices (PLD), Programmable logic array able logic devices (CPLDS), Field Programma State diagram, State table, State reduction, presentation, Implementation, finite state management of the programma of the	shronoundle Logy, Prograble Contact a chine in lication	ogic Deramma date Anassigne mplem	evice able a rray (ment, entati	s: ROM a array logic FPGA)
Unit IV Unit V Text Book	Types of Memory of Programmable logic complex Programmable Basic design steps-Moore machines rep Sequence detector. Morris Mano: "A Herbert Taub, Done	lown counters, twisted ring counters lown counters, synchronous counters. Asynchronous counters. Asynchronous counters. Asynchronous counters. Asynchronous counters. Asynchronous counters. Asynchronous counters. Programma de devices (PLD), Programmable logic array able logic devices (CPLDS), Field Programmable logic array able logic devices (CPLDS), Field Programmable logic devices (CPLDS), Field Programmable logic array able logic devices (CPLDS), Field Programmable logic devices (CPLDS), Field Prog	shronoundle Logy, Progradule Control of the Control	ogic Doramma Gate Anassignr mplem	evice able a rray (ment, entati	s: ROM a array logic FPGA)
Unit IV Unit V Text Book	Types of Memory of Programmable logic complex Programmable Basic design steps-Moore machines rep Sequence detector. Morris Mano: "A Herbert Taub, Done	lown counters, twisted ring counters lown counters, synchronous counters. Asynchronous counters. Asynchronous counters. Asynchronous counters. Programma de devices (PLD), Programmable logic array able logic devices (CPLDS), Field Programma State diagram, State table, State reduction, presentation, Implementation, finite state management of the programma of the	shronoundle Logy, Progradule Control of the Control	ogic Doramma Gate Anassignr mplem	evice able a rray (ment, entati	s: ROM a array logic FPGA)
Unit IV Text Book T.1 T.2 T.3	Types of Memory of Programmable logic complex Programmable logic complex Programma Basic design steps-Moore machines rep Sequence detector. Morris Mano: "A Herbert Taub, Done W. Fletcher: "Engle Books"	lown counters, twisted ring counters lown counters, synchronous counters. Asynchronous counters. Asynchronous used memory chips. Programma de devices (PLD), Programmable logic array able logic devices (CPLDS), Field Programma State diagram, State table, State reduction, presentation, Implementation, finite state management of the digital Design, Pearson Published Land Counter (PLDS), Programmable logic array able logic devices (CPLDS), Field Program	chronoundle Logonable Control State a chine in McGraw cations.	ogic Drammadate Amassignamplem	evice able a rray (ment, entati	s: ROM a array logic FPGA) Mealy an
Unit IV Unit V Text Book T.1 T.2	Types of Memory of Programmable logic complex Programmable logic complex Programma Basic design steps-Moore machines reprogramma Sequence detector. Morris Mano: "A Herbert Taub, Done W. Fletcher: "En Books Wakerly Pearon Publications.	down counters, twisted ring counters. Asynchon counters, synchronous counters. Asynchon commonly used memory chips. Programma de devices (PLD), Programmable logic array able logic devices (CPLDS), Field Programmable logic devices (CPLDS), F	shronoundle Logy, Progradule Control of the Logical Control of the Logical of the	ogic Drammadate Anassignramplem	evice able a rray (ment, entati	s: ROM a array logic FPGA) Mealy an
Unit IV Text Book T.1 T.2 T.3 Reference	Types of Memory of Programmable logic complex Programmable logic complex Programma: Basic design steps-Moore machines rep Sequence detector. Morris Mano: "A Herbert Taub, Done W. Fletcher: "En Books Wakerly Pearon Publications. Mark Bach: "Con	lown counters, twisted ring counters lown counters, synchronous counters. Asynchronous counters. Programma common counters (PLD), Programmable logic array able logic devices (CPLDS), Field Programmable logic array able logic	shronoundle Logy, Progradule Control of the Logical Control of the Logical of the	ogic Drammadate Anassignramplem	evice able a rray (ment, entati	s: ROM a array logic FPGA) Mealy an
Unit IV Text Book T.1 T.2 T.3 Reference R.1	Types of Memory of Programmable logic complex Programmable logic complex Programma: Basic design steps-Moore machines rep Sequence detector. Morris Mano: "A Herbert Taub, Done W. Fletcher: "En Books Wakerly Pearon Publications. Mark Bach: "Con	down counters, twisted ring counters. Asynchon counters, synchronous counters. Asynchon commonly used memory chips. Programma de devices (PLD), Programmable logic array able logic devices (CPLDS), Field Programmable logic devices (CPLDS), F	shronoundle Logy, Progradule Control of the Logical Control of the Logical of the	ogic Drammadate Anassignramplem	evice able a rray (ment, entati	s: ROM a array logic FPGA) Mealy an
Unit IV Unit V Text Book T.1 T.2 T.3 Reference R.1 R.2	Types of Memory of Programmable logic complex Programmable logic complex Programma Basic design steps-Moore machines rep Sequence detector. Morris Mano: "A Herbert Taub, Done W. Fletcher: "En Books Wakerly Pearon Publications. Mark Bach: "Con R.P. Jain: "Mode nks	down counters, twisted ring counters alown counters, synchronous counters. Asynchronous counters. Asynchronous used memory chips. Programma able logic devices (PLD), Programmable logic array able logic devices (CPLDS), Field Programmable lo	shronoundle Logy, Progradule Control of the Logical Control of the Logical of the	ogic Drammadate Anassignramplem	evice able a rray (ment, entati	s: ROM a array logic FPGA) Mealy an
Unit IV Unit V Text Book T.1 T.2 T.3 Reference R.1 R.2 R.3	rippleCounters, up/d Types of Memory of Programmable logic complex Programma Basic design steps- Moore machines rep Sequence detector. Morris Mano: "A Herbert Taub, Done W. Fletcher: "En Books Wakerly Pearon Publications. Mark Bach: "Cook R.P. Jain: "Mode onks https://nptel.ac.in.	lown counters, twisted ring counters lown counters, synchronous counters. Asynchronous counters. Programma common counters (PLD), Programmable logic array able logic devices (CPLDS), Field Programmable logic array able logic	shronoundle Logy, Progradule Control of the Logical Control of the Logical of the	ogic Drammadate Anassignramplem	evice able a rray (ment, entati	s: ROM a array logic FPGA) Mealy an

	Course Outcomes
EC2305.1	Apply the knowledge of digital Electronics and Component working.
EC2305.2	Solve the problems of expression by using K map technique.
EC2305.3	Design flip-flops with the help of Logic Gates.
EC2305.4	Structure the design of memories by using digital electronics.
EC2305.5	Analyze and Design state diagrams with the help of digital techniques.

- Phartment of Electronics & Comm Tuistramji Gaikwad - Panii College of Engineering & Technology, Nagpur Dean Agademics

Dean Academics

Vice Principal Vice Principal

Principal

Tulsiramji Gaikwad-Patil Tulsiramji Gaikwad-Patil Julsiramji Gaikwad Patil College College Of Engineering College Of Engineering Engineering and Technology, Nagpur Technology, Nagpur.



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		NAAC Accredited (A+ Grade)	na				
Progran	n: B. Tech Electro	onics & Communication Engineeri	ng	1 00	-	G 1'-	
Semester	Course Code	Name of Course	L	T	P	Credits	
III	EC2306	Electronic Devices and Circuit	4	-	-	4	
		Course Contents					
Unit I	Semiconductor Diodes: PN junction diode and its application, Physics and structure or diodes, Diode small signal model, Zener diode, Rectifier circuits, Clipping and clamping circuits.						
Unit II	model Current voltage characteristics of CE, CB, CC configuration,						
Unit III	Junction Field-effect Transistor: JFET parameters, C-V characteristics, Biasing of JFE1,						
Unit IV	Power Amplifier: Class A, Class B, Class AB and Class C, Power Efficiency, Power Dissipation, Cross-Over Distortion in Class AB Circuits, , Oscillators: RC, LC, Crystal Oscillators						
Unit V	MOSFET: Device Structure and Physical Operation of MOSFET, Finite Output Resistance in Saturation, I-V Characteristics of the MOSFET, Overview of D-MOSFET, E-MOSFET						
Text Book	Š						
T.1	"Electronic Devices	s and Circuits", "Millman Halkias", "TMI-	f", 2000)			
T.2		s and Circuits", "David A. Bell", "PHI", 4					
T.3	Electronics Devices and Circuit-Jimmie J.Cathey, McGraw - Hill Education						
Reference	A STATE OF THE PARTY OF THE PAR		4				
R.1	Electronic Devices and Theory - BoyleStad, Nashelsky 9th. Edition May 2010 PHI						
R.2	Electronic Devices and Theory - Boylestad, Nashelsky star Burden Start Electronic Devices and Circuits - S Salivahanan, N Suresh Kumar 3rd Edition Tata McGraw Hills						
R.3	Electronic Devices and Theory – V.K. Mehta 3rd Edition McGraw – Hill						
seful Link							
1		ourses/108/104/108104139/					
2	http://nptel.ac.in/cou						
3	http://nptel.ac.in/cou						
	into an interest in the second					7-15	
		Course Outcomes				-	
CC2306.1	Apply the concept o	f various semiconductor devices.					
C2306.2	Analyze Transistor (Characteristics and its operating mode for	design	differ	ent cir	cuits.	

EC2306.3	Evaluate the response of transistors at low and high frequency.
EC2306.4	Construct Power amplifiers and oscillators.
EC2306.5	Analyze the operation of MOSFET in various regions.

Department of Electronics & Comm. Tuisiraniji Gaikwad - Paril College of Engineering & Technology, Nagpur

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Technology



Wardha Road, Nagpur-441 108
NAAC Accredited (A+ Grade)

Program: B. Tech Electronics & Communication Engineering

Semester	er Course Code Name of Course L T P Cre					Cred	edits	
Ш	EC2307	Object Oriented programming Structure Lab			2		1	
Sr. No.		List of Experiment					со	
1	Setup a Java programming development environment and test using small program.						COI	
2	Develop programs to demonstrate use of if statement and its different forms.							
3	Develop programs to demonstrate use of looping statement'for'							
4	Develop a program for implementation of Wrapper Class to convert primitive into object and object into primitive.							
5	Develop a program for the concept of class constructor and its type in Java							
6	Develop a program to implement the Overloading							
7	Develop a program for implementation of Single andMultilevel inheritance						CO4	
8	Develop a program for implementation of multiple inheritances.						CO	
9	Develop a program for implementation of I/0 stream and file stream classes.						CO	
10	Develop a program for exception handling						CO	
Text Boo	oks							
T.1	Java: The Comp	lete Reference" by Herbert Schildt						
T.2	Programming with Java" by Balagurusamy							
T.3	Core Java for the Impatient" by Horstmann							
T.4	Head First Object-Oriented Analysis and Design							
Referen	ce Books							
R.1	Thinking in Java by Bruce Eckel							
R.2	Java Q for Programmers by Paul Deitel, Harvey Deite						1	
Ŗ.3	Beginning Java Programming: The Object-Oriented Approach by Bart Baesens, Aimee Backiel, a Seppe vanden Broucke						er, and	
Useful L								
1	https://nptel.ac.in.	/courses/106/102/106102064/					-	
2	https://nptel.ac.in/	/courses/106/106/106106145/						
3	https://nptel.ac.in.	/courses/106/105/106105085/						
	C. A. S. S. S.	Course Outo	omes					

EC2307.2	Apply the knowledge of programming and controlling statements in Java.
EC2307.3	Analyze the Concept of programming.
EC2307.4	Apply the knowledge of Inheritance in program development.
EC2307.5	Illustrate the knowledge to handle various exceptions using concepts of exception handling

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Progra	m: B. Tech Elec	tronics & Communication Engineering	ng					
Semeste	r Course Code	Name of Course	L	T	P	Credits		
Ш	EC2308	Electronic Devices and Circuit Lab	-	-	2	1		
Sr. No.		List of Experiment				co		
1	Examine the V- I characteristics of PN junction diode (Silicon) and Zener diode.							
2	To evaluate Voltage regulation of a Zener diode							
3	To Design Full Wave Rectifier with filter							
4	To analyze I/P & O/P Characteristics of BJT Common Base Transistor Configuration							
5	To analyze I/P & O/P Characteristics of BJT Common Emitter Transistor Configuration							
6	To analyze Junction Field Effect Transistor Characteristics							
7	To Design Class B Amplifier with Cross Over Distortion.							
8	To Design Class C Amplifier with crossover Distortion.							
9	To evaluate the concept of LC Oscillators.							
10	To structure Drain and Transfer characteristics of Metal Oxide Semiconductor Field Effect Transistor (MOSFET).							
Text Bo			11			A		
T.1	Engineering Circuit Analysis - William H. Hayt, 8th Edition McGraw - Hill							
T.2		es and Circuit-Jimmie J.Cathey, McGraw - Hi	II Edu	cation				
Referen	ce Books		ion M	201	IN PHI			
R.1								
R.2	Electronic Devices and Circuits - S Salivahanan, N Suresh Kumar 3rd Edition Tata McGraw Hills							
Useful I			-					
1	https://nptel.ac.in/	courses/108/104/108104139/		-				
2	http://nptel.ac.in/c	ourses/117107095				E SE		
		Course Outcomes						
EC2308	.1 Apply the conc	ept of various semiconductor devices.				11111		
EC2308	.2 Analyze Transi	stor Characteristics and its operating mode for	desig	n diffe	erent c	ircuits.		
EC2308	.3 Evaluate the re	sponse of transistors at low and high frequenc	у.					

EC2308.4	Construct Power amplifiers and oscillators.
EC2308.5	Analyze the operation of MOSFET in various regions.

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Vice Principal Vice Principal

Tulsiramji Gaikwad-Patil Tulsiramji Gaikwad-Patil

Principal

Principal

College Of Engineering College Of Engineering Austramji Gaikwad Patil College and Technology, Nagpur, Engineering and Technology, Nag and Technology, Nagpur





Program	n· R Tech F	NAAC Accredited (A+ Grade) Clectronics & Communication Engineering	ng	Will.			
Semester		Name of Course	L	T	P	Credits	
III	EC2309	Digital Electronics & Memories Lab	-	-	2	1	
Sr. No.		List of Experiment				CO	
1	Construct & verify the truth table of different Logic Gates						
2	Design & implement universal gates.						
3	Verify proof o	of demorgan's theorem		. W.		COI	
4	Design & verif	y truth table of Multiplexer and Demultiplexer				CO2	
5	Design & Exec	cute Adder and substractor Circuits.				CO2	
6	Design Circuit	for Code converter.				CO2	
7	Design Circuit for Conversion of Flip Flop						
8	Construct and design Flip Flop Circuit.						
9	Experiment Based on transient state diagram.						
10	Verify the application of state diagram.						
Text Boo	ks						
T.1	Morris Mane	o: "An approach to digital Design", Pearson Publ	ications				
T.2	Ramesh Gao	onkar: "Microprocessor Architecture, Programminam International Publications	ng and A	Applic	ations	with the	
Reference	e Books						
R.1	Wakerly Pea	aron: "Digital Design: Principles and Practices", F	earon E	ducati	ion Pu	iblications	
R.2	R.P. Jain: "Modern digital electronics", TMH Publications.						
Useful L	inks						
1		ac.in/courses/117/106/117106114/					
2	https://nptel.ac.in/courses/117/106/117106086/						
		Course Outcomes		4.5.			
EC2309.	1 Apply the	knowledge of digital Electronics and Component	working	g.			
EC2309.	Solve the	problems of expression by using K map technique					
EC2309.	Design fli	p-flops with the help of Logic Gates.					

EC2309.4	Structure the design of memories by using digital electronics.	7-21
EC2309.5	Analyze and Design state diagrams with the help of digital techniques.	

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		onics & Communication Enginee					
Semeste	Course Code	Name of Course	L	T	P	C	redits
III	EC2310	Instrument and Measurement Lab	-	-	2		1
Sr. No.		List of Experiment					co
1	To Examin the functi	on of Analog Meter & Digital Multimete	r				CO
2	To Measure Medium Resistance by using voltmeter ammeter method and by colour coding.						COS
3	To Measure Medium	Resistance by using Wheatstone bridge r	nethod	1.			CO
4	To Measure Low Res	istance by using Kelvin Bridge Method.		-			CO:
5	To Measure Unknown inductance by using Hay's Bridge / Maxwell Bridge Method.						
6	To Measure Unknown Capacitance by using Schering Bridge/Desauty bridge Method CO3						
7	To Measure Tempera	ture & control using RTD / Thermocoupl	e / Th	ermist	or.		CO
8	To Measure Displacement using LVDT, Level measurement using capacitive / resistive transducer						
9	To determine the frequency of unknown signal using Lissagious Pattern Method To Determine DC Voltage, AC voltage and phase by using CRO						CO:
10	To Measure signal parameters using Digital Storage Oscilloscope & Study of Data Acquisition system & Feature extraction of Some standard signal using Spectrum Analyzer						CO:
Text Boo	ks						
1	A.D. Helfrick and W.D. Cooper: "Modern Electronic Instrumentation and Measurement Techniques", PHI Publications.						
2	A.K. Sawhney: "Electrical and Electronic Measurement and Instrumentation", Dhanpat Sons Publications.						
Reference							
	Joseph J. Carr: "Elements of Electronic Instrumentation and Measurement", Pearson Ed Publications.						ducatio
		ical And Electronic Measurement", PHI I	Publica	ations.			
Useful L	inks						
	https://nptel.ac.in/cou	rses/108/108/108108147/					
2	https://nptel.ac.in/cou	rses/108/105/108105153/					
		Course Outcomes					
EC2310.	1 Examine measuri	ng methods and instruments of electrical	quanti	ities.			
EC2310.	2 Determine design	aspects and performance criterion of me	asurin	ıg instı	ument	s.	

EC2310.3	Design the different types of bridges.
EC2310.4	Test the working principle of various transducers.
EC2310.5	Analyze CRO, signal generator, wave analyzer and data acquisition system.

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BOS Chairman
HOD
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Vice Principal
Vice